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Date: August 24, 2001

By: Patrick D. Benedicto
Patrick D. Benedicto

In re Application of:	
Craig L. Stevens, et al.	
Serial No. 09/872,796	Examiner: Unknown
Filed: June 1, 2001	Art Unit: 2812
Title: Architecture For High Throughput Semiconductor Processing Applications	

Commissioner for Patents
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2. There is an error in that the following data is:
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Architecture for High Throughput Semiconductor Processing Applications

3. Please issue a corrected Filing Receipt rectifying the above error. The requested correction is indicated in red ink in the enclosed copy of the Filing Receipt.

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Dated: August 24, 2001

Respectfully submitted,

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/872,796	06/01/2001	2812	862	50544.00004 (NVLS 379)	15	24	4

DATES ENTERED:

No Action

CONFIRMATION NO. 4156

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FILING RECEIPT



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BY MD
ATTORNEY
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Date Mailed: 08/02/2001

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Applicant(s)

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Karl B. Levy, Los Altos, CA;

Domestic Priority data as claimed by applicant

THIS APPLN CLAIMS BENEFIT OF 60/209,079 06/02/2000

Foreign Applications

If Required, Foreign Filing License Granted 08/02/2001

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Architecture for high throughput semiconductor processing applications

APPLICATIONS

Preliminary Class

438

Data entry by : TESFAYE, ALEM

Team : OIPE

Date: 08/02/2001





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